

Application No.: 09/785,162

Docket No.: 20402-00623-US

**REMARKS**

The Office Action and prior art relied upon have been carefully considered.

Claim 1 has been rejected under 35 U.S.C. §102(b) as anticipated by Rocco (US 5,642,386). Claim 2-6 have been rejected under 35 U.S.C. §103(a) as being unpatentable over Rocco. Claims 4-6 been rejected under 35 U.S.C. §103(a) as being unpatentable over Rocco in view of Lee (US 6,266,799).

In rejecting claims 1 and 4 under 35 U.S.C. 102(b), the examiner states that Rocco, discloses "a clock generating --- received data", "a delay circuit --- delayed data", "a first sampling circuit --- data sampling value", "a second sampling --- delayed data sampling value", and "receiving data judging means --- fails to judge the received data value." The examiner relies on Fig. 1, elements 23, 24; col. 5, lines 62-65; col. 6, line 60 to col. 7, line 43 to demonstrate the reference's disclosure of the presently claimed "received data judging means."

However, there is no description with respect to the first judging means and the second judging means constituting the received data judging means of the present invention.

According to the present invention defined in independent claim 1 and 4, the first judging means judges the received data value primarily based on either one of said received data sampling value and said delayed data sampling value. The second judging means judges the received data value secondarily based on the other of said received data sampling value and said delayed data sampling value when said first judging means fails to judge the received data value.

Rocco does not teach a combination of such first (i.e. primary) and second (i.e. secondary) judging means or devices which are complementarily arranged for judging a received data value.

With respect to dependent claims 2, 3, 5 and 6, Rocco does not teach using both rise and fall edges (i.e. leading and trailing edges) of the clock. With respect to dependent claims 3 and 6, the reference is silent as to the use of a clock frequency that is approximately twice the frequency of the data transmission rate of the received data.

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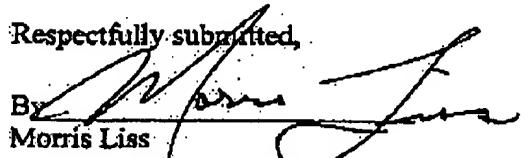
In view of the foregoing points raised by the applicant, the present invention defined in claims 1-6 is clearly and patentably distinguished over the cited references.

In view of the above amendment, applicant believes the pending application is in condition for allowance.

Applicant believes no fee is due with this response. However, if a fee is due, please charge our Deposit Account No. 22-0185, under Order No. 20402-00623-US from which the undersigned is authorized to draw.

Dated: July 16, 2004

Respectfully submitted,

By 

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